

DEC 06 2004

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THE UNITED STATES PATENT AND TRADEMARK OFFICE

DATE: 12/6/2004

RE: Serial No.: 09/822473Docket No.: NL00 0182TO: Examiner: Nguyen, LeeArt Unit: 2683Fax Number: (703) 872-9306FROM: Michael J. Ure, Reg. No. 33,089Telephone: (408) 474 - 9077TRANSMISSION INCLUDES: 16 Pages (including cover sheet)Brief for Appellant (in triplicate) - 5 pages

CERTIFICATE OF TRANSMISSION UNDER 37 CFR 1.8	
I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office at the number listed above	
on <u>12/6/04</u>	2004 by <u>[Signature]</u>
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of
KIANUSH

Atty. Docket
NL00 0182

Serial: 09/822,473

Group Art Unit: 2682

Filed: 03/30/2001

Examiner: NGUYEN, LEE

NARROW BAND AM FRONT END

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Charge Authorization

The Commissioner is hereby requested and authorized pursuant to 37 CFR §1.136(a)(3), to treat any concurrent or future reply in this application requiring a petition for extension of time for its timely submission, as incorporating a petition for extension of time for the appropriate length of time. Please charge any additional fees that may now or in the future be required in this application, including extension of time fees, but excluding the issue fee unless explicitly requested to do so, and credit any overpayment, to Deposit Account No. 14-1270.

BRIEF FOR APPELLANT

Sir:

Pursuant to the Notice of Appeal filed October 4, 2004, the present Brief for Appellant is submitted herewith.

REAL PARTY IN INTEREST

The real party in interest is the assignee, Philips Electronics North America Corporation.

RELATED APPEALS AND INTERFERENCES

Applicant is not aware of any related appeals or interferences.

STATUS OF CLAIMS

Claims 1-3 are currently pending. All claims have been rejected and the rejection of all claims has been appealed.

STATUS OF AMENDMENTS

All amendments have been entered. The Response After Final of 07/26/2004 made no amendments.

SUMMARY OF CLAIMED SUBJECT MATTER

The present invention relates to a receiver having an RF stage for receiving an antenna signal from an inductive antenna (Fig. 2, AN), a processing stage for processing the output signals of the RF stage (Fig. 2, PRS) and an output (Fig. 2, O) for supplying an audio signal. The RF stage includes electronically switched capacitors (Fig. 2, IRFS), controlled by a switch control circuit (Fig. 2, SCC), for adjusting front-end selectivity of the RF stage to correspond to an established tuning frequency.

GROUND OF REJECTION TO BE REVIEWED ON APPEAL

Review is respectfully requested of the following grounds of rejection:

Claims 1 and 3, rejected as being anticipated by Yasooka.

Claim 2, rejected as being unpatentable over Yasooka in view of MacDonald.

ARGUMENT

Claims 1 and 3 are Not Anticipated by Yasooka

There is no switching of capacitors under electronic control in Yasooka. Rather, the switch 12c is, from all indications, manually switched. Furthermore, the "frequency changing circuit" 3 of Yasooka is simply a well-known downconverter, i.e., RF mixer. It does not in any way affect the capacitors 12a and 12b. Note the description on the bottom third of page 2 of the translation of Yasooka, which describes the received frequency f_1 being downconverted to an intermediate frequency f_3 using a local oscillator signal of frequency f_2 , where $f_3 = f_2 - f_1$.

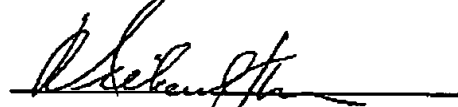
Claim 2 is Patentable Over Yasooka in View of MacDonald

MacDonald was cited for teaching more than two capacitors and switches used for tuning different channels. MacDonald, however, does nothing to remedy the deficiencies of the primary reference, Yasooka.

CONCLUSION

For the foregoing reasons, Appellant submits that the final rejection should be REVERSED.

Respectfully submitted,

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Michael J. Ure, Reg. 33,089

Dated: November 23, 2004

APPENDIX OF CLAIMS

1. Receiver comprising an RF stage for receiving an antenna signal from an inductive antenna, a processing stage for processing the output signals of the RF stage and an output for supplying an audio signal, characterized in that the RF stage comprises electronically switched capacitors, controlled by a switch control circuit, for adjusting front-end selectivity of the RF stage to correspond to an established tuning frequency.
2. Receiver as claimed in claim 1, characterized in that the switch capacitors of the RF stage comprises at least two series arrangements of a capacitor and switching means parallel whereby the switches are coupled to the switch control circuit.
3. RF stage for use in a receiver according to claim 1.

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